

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
1875.138000FAPPLICATION NO.
(to be assigned)APPLICANTS
Ralph Duncan *et al.*FILING DATE
HerewithGROUP
(to be assigned)

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1	4,211,975	07/1980	Kuroda			
	AB1	4,789,976	12/1988	Fujishima			
	AC1	4,818,903	04/1989	Kawano			
	AD1	4,903,329	02/1990	Marik <i>et al.</i>			
	AE1	5,031,233	07/1991	Ragan			
	AF1	5,200,826	04/1993	Seong			
	AG1	5,311,318	05/1994	Dobrovolsky			
	AH1	5,329,143	07/1994	Chan <i>et al.</i>			
	AI1	5,389,898	02/1995	Taketoshi <i>et al.</i>			
	AJ1	5,423,076	06/1995	Westergren <i>et al.</i>			
	AK1	5,428,829	06/1995	Osburn <i>et al.</i>			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1	37 23 778 A1	01/1988	DE	H03L	7/10	X Yes No
	AM1	195 06 324 C1	10/1995	DE	H03H	21/00	X Yes No
	AN1	0 393 717 A2 & A3	10/1990	EP	H03H	11/12	Yes No
	AO1	0 431 887 A2 & A3	06/1991	EP	H03L	1/02	Yes No
	AP1	0 535 536 A1	04/1993	EP	H01L	27/02	Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	1	Burghartz, J. N. <i>et al.</i> , "High-Q Inductors in Standard Silicon Interconnect Technology and its Application to an Integrated RF Power Amplifier," <i>International Electron Devices Meeting Technical Digest</i> , IEEE, pp. 1015-1017 (December 10-13, 1995).
	AS	1	Craninckx, J. and Steyaert, M.S., "A 1.8-GHz Low-Phase-Noise CMOS VCO Using Optimized Hollow Spiral Inductors," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 32, No. 5, IEEE, pp. 736-744 (May, 1997).
	AT	1	Long, J.R. and Copeland, M.A., "The Modeling, Characterization, and Design of Monolithic Inductors for Silicon RF IC's," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 32, No. 5, IEEE, pp. 357-369 (March, 1997).

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	AA2	5,446,311	08/1995	Ewen <i>et al.</i>			
	AB2	5,483,207	01/1996	Gabara			
	AC2	5,625,325	04/1997	Rotzoll <i>et al.</i>			
	AD2	5,630,214	05/1997	Yamamoto <i>et al.</i>			
	AE2	5,648,744	07/1997	Prakash <i>et al.</i>			
	AF2	5,686,864	11/1997	Martin <i>et al.</i>			
	AG2	5,715,012	02/1998	Patel <i>et al.</i>			
	AH2	5,717,243	02/1998	Lowther			
	AI2	5,737,035	04/1998	Rotzoll			
	AJ2	5,739,587	04/1998	Sato			
	AK2	5,825,600	10/1998	Watt			

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2	0 629 040 A1	12/1994	EP	H03D	1/22	Yes No
	AM2	0 780 853 A1	06/1997	EP	H01F	17/00	Yes No
	AN2	2 058 505 A	04/1981	GB	H03D	7/12	Yes No
	AO2	62-154607	07/1987	JP	H01F	15/04	X Yes No
	AP2	WO 97/09786 A1	03/1997	PCT	H03L	7/08	Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	2	Poole, S.J. <i>et al.</i> , "A CMOS Subscriber Line Audio Processing Circuit Including Adaptive Balance," <i>IEEE International Symposium on Circuits and Systems Proceedings</i> , Vol. 2 of 3, IEEE, pp. 1931-1934 (1988).
	AS	2	Yue, C.P. and Wong, S.S., "On-Chip Spiral Inductors with Patterned Ground Shields for Si-Based RF IC's," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 33, No. 5, IEEE, pp. 743-752 (May, 1998).
	AT	2	English-language Abstract of Japanese Patent Publication No. 62-154607, 2 pages (July 9, 1987) - Date of Publication of Application).

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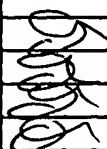
APPLICATION NO.
(to be assigned)

APPLICANTS
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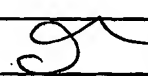
FILING DATE
Herewith

GROUP
(to be assigned)

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA3	5,828,589	10/1998	Degenhardt			
	AB3	6,150,891	11/2000	Welland <i>et al.</i>			
	AC3	6,177,964 B1	01/2001	Birleson <i>et al.</i>			
	AD3	6,426,680 B1	07/2002	Duncan <i>et al.</i>			
	AE3						
	AF3						
	AG3						
	AH3						
	AI3						
	AJ3						
	AK3						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL3	WO 98/50956 A1	11/1998	PCT	H01L	29/00	Yes No
	AM3						Yes No
	AN3						Yes No
	AO3						Yes No
	AP3						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

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